



TPR-CD01191M

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

Matthew PLEIS et al.

SERIAL NO: 09/989,817

FILED: NOVEMBER 19, 2001

FOR: A SYSTEM AND METHOD OF  
DYNAMICALLY  
RECONFIGURING A  
PROGRAMMABLE SYSTEM ON  
A CHIP

:

: GROUP ART UNIT:

:

: EXAMINER:

**RECEIVED**

**MAR 03 2003**

**Technology Center 2100**

PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following Amendments and Remarks is respectfully requested. (In determining line numbers, only those lines with text were included; blank lines were not counted.)

IN THE TITLE

Please replace the original title with the following new title:

A SYSTEM AND METHOD OF DYNAMICALLY RECONFIGURING A  
PROGRAMMABLE INTEGRATED CIRCUIT

IN THE SPECIFICATION

Please replace the paragraph beginning at page 1, line 1 with the following:

A SYSTEM AND METHOD OF DYNAMICALLY RECONFIGURING A  
PROGRAMMABLE INTEGRATED CIRCUIT